

FORM PTO-1449 (REV. 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. T&A-122	SERIAL NO. 10/701511
LIST OF DOCUMENTS CITED BY APPLICANT (Use several sheets if necessary)		APPLICANT R. TAKEMURA et al	
		FILING DATE November 6, 2003	GROUP 2827

U.S. PATENT DOCUMENTS

* EXAMINER INITIAL	DOCUMENT	DATE	NAME	CLASS	SUBCLASS	FILING DATE (If Appropriate)
	AA					
	AB					
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					
	AK					

FOREIGN PATENT DOCUMENTS

	DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
TL	AL 2001-256782	09/21/01	Japan (w/English Abstract)			<input type="checkbox"/>	<input type="checkbox"/>
	AM					<input type="checkbox"/>	<input type="checkbox"/>
	AN					<input type="checkbox"/>	<input type="checkbox"/>
	AO					<input type="checkbox"/>	<input type="checkbox"/>
	AP					<input type="checkbox"/>	<input type="checkbox"/>

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

TL	AR	"WP 24.1 An 8ns Random Cycle Embedded RAM Macro with Dual-Port Interleaved DRAM Architecture (D ² RAM)", 2000 IEEE International Solid-State Circuits Conference, 07803-5853-8/00.
	AS	
	AT	

EXAMINER Thoyke	DATE CONSIDERED 3/9/05
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* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.